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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of
Lasserre

Serial No.: 09/932,381

Filed: August 17, 2001

For: Local Memory With Indicator Bits to Support Concurrent DMA and
CPU Access

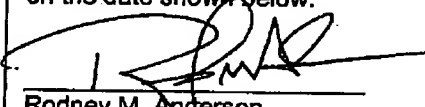
Docket Number: TI-31354

Art Unit: 2188

Examiner: Bragdon

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Rodney M. AndersonSeptember 15, 2004
Date**FACSIMILE COVER SHEET**

☒ **FACSIMILE COVER SHEET**
☐ NEW APPLICATION
☐ DECLARATION
☐ ASSIGNMENT
☐ FORMAL DRAWINGS
☐ INFORMAL DRAWINGS
☐ CONTINUATION APP'N
☐ DIVISIONAL APP'N

☒ **REPLACEMENT AMENDMENT**
(9 Pages)
☐ EOT
☐ NOTICE OF APPEAL
☐ APPEAL
☐ ISSUE FEE

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